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Document(s) Attached: Response to Restriction Requirement Total Pages (incl. this Sheet): 3

In re application of:	Huajie Chen et al.
Serial Number:	10/711,637
Filing Date:	September 29, 2004
Attorney:	Todd M. C. Li IBM Corporation Dept.18G, B/321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533

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EXAMINER ART UNIT: FAX NO:	Tsz K. Chu 2822 571-273-8300
FROM:	Daryl K. Neff 4 Eugene Circle Lincoln Park, NJ 07035 Phone 973-316-2612

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Name of Depositor:

Daryl K. Neff

Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE		
In re application of: Huajie Chen et al.	Date: January 6, 2006	
Serial Number: 10/711,637	Examiner: Tsz K. Chiu	
Filed: September 29, 2004	Group Art Unit: 2822 / Conf #5636	
Title: STRUCTURE AND METHOD FOR MAKING STRAINED CHANNEL FIELD EFFECT TRANSISTOR USING SACRIFICIAL SPACER	Todd M. C. Li IBM Corporation Dept. 18G, Bldg. 321, Zip 482 2070 Route 52 Hopewell Junction, NY 12533	

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In the Restriction Requirement dated December 6, 2005, the Examiner requires restriction to one of two patentably distinct inventions:

Group I, claims 1-14 drawn to a field effect transistor device; and

Group II, claims 15-29 drawn to a method of making the FET device.

In response, Applicants elect the Group I claims 1-14 for examination without traverse.

Prompt examination of this application is respectfully requested.

It is believed that no fee is due in connection with the filing of this response. However, if any fee is due, please debit the Deposit Account No. 09-0458 of the Assignee International Business Machines Corporation.

Respectfully submitted,

Huajie Chen et al.

Daryl K. Neff, Attorney

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